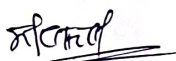


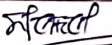
MTech Semester I (Communication and Signal Processing) Room – LH7 Langol

Day	900-1000	1000-1100	1100-1200	1200-1300	1-2 PM	1400-1500	1500-1600	1600-1700
M	EC 501, Advanced Antenna Design (3-0-0-6) Dr Kalyan Mondal	EC 507, Signal Processing Algorithm (3-0-0-6) Dr K PritamDas	EC557 Fiber optics communication S Jayananda	Lunch	Lunch	EC517, Communication System Lab (0-0-3-3)Dr.A Dinamani Singh		
T	EC 501, Advanced Antenna Design (3-0-0-6) Dr Kalyan Mondal	EC 503, Information theory and coding (3-0-0-6) Dr Kh Jilenkumari	EC557 Fiber optics communication S Jayananda			Library		
W	EC 503, Information theory and coding (3-0-0-6) Dr Kh Jilenkumari	EC557 Fiber optics communication S Jayananda	EC505, Modern Wireless Communication(3-0-0-6)Dr.A Dinamani Singh			Library		
T	EC 507, Signal Processing Algorithm (3-0-0-6) Dr K PritamDas	EC 503, Information theory and coding (3-0-0-6) Dr Kh Jilenkumari	EC505, Modern Wireless Communication(3-0-0-6)Dr.A Dinamani Singh			Library		
F	EC 507, Signal Processing Algorithm (3-0-0-6) Dr K PritamDas	EC 501, Advanced Antenna Design (3-0-0-6) Dr Kalyan	Mondal EC505, Modern Wireless Communication(3-0-0-6) Dr.A Dinamani Singh			Library		


Dr.A.Dinamani Singh
(HOD,ECE Dept)

3rd Semester I (VLSI and Embedded system) Room – VLSI Lab

Day	900-1000	1000-1100	1100-1200	1200-1300	1-2 PM	1400-1500	1500-1600	1600-1700
M	VL 503 Embedded Systems Design(3-0-0-6)Dr.L Suraj kumar	VL501.Analog and Digital CMOS IC Design (3-0-0-6) Dr. Manoj Kumar	VL 505, Semiconductor device Modeling (3-0-0-6) Dr. Chitralekha Ngangbam	lunch	lunch			
T	VL507,VLSI System Design (3-0-0-6) Dr. Ashish Ranjan	VL 505, Semiconductor device Modeling (3-0-0-6) Dr. Chitralekha Ngangbam	VL 523, Signal Processing for Embedded System(3-0-0-6) Dr S Devayani			VL511,VLSI and Embedded Lab -I 0-0-3-3) Dr. Manoj		
W	VL 523. Signal Processing for Embedded System(3-0-0-6) Dr S Devayani	VL501.Analog and Digital CMOS IC Design (3-0-0-6) Dr. Manoj Kumar	VL 503 Embedded Systems Design(3-0-0-6)Dr.L Suraj kumar					
T	VL501.Analog and Digital CMOS IC Design (3-0-0-6) Dr. Manoj Kumar	VL 523, Signal Processing for Embedded System(3-0-0-6) Dr S Devayani	VL507,VLSI System Design (3-0-0-6) Dr. Ashish Ranjan					
F	VL 505, Semiconductor device Modeling (3-0-0-6) Dr. Chitralekha Ngangbam	VL 503 Embedded Systems Design(3-0-0-6)Dr.L Suraj kumar	VL507,VLSI System Design (3-0-0-6) Dr. Ashish Ranjan					


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